

ARM Cortex-A9 IMP-Master



IMP-MAS3 is an IMP master with a powerful ARM CPU for applications of basic to medium complexity. As a fieldbus controller for GinLink, IMP-MAS3 controls and coordinates the entire periphery. In addition, up to 16 IMP participants can be directly addressed via the local IMP bus. The power supply for additional participants is already integrated into this module.

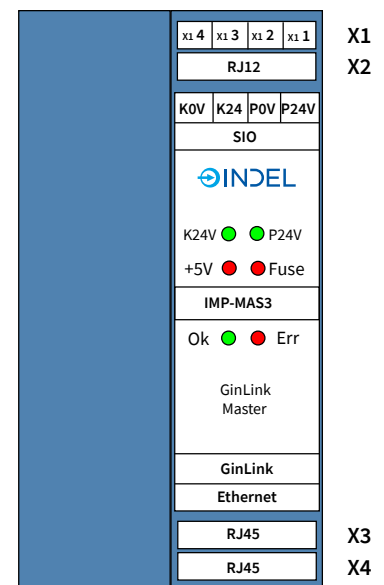
IMP-MAS3 GIN



Technical data

Interfaces	1 × GinLink 1 × Gigabit Ethernet 1 × SIO (RS232 / RS422) 1 × IMP-Bus
Real-time clock	Yes
CPU	ARM Cortex-A9 dual-core, 800 MHz
Memory	256 MByte DDR-RAM 8 MByte NOR-Flash 512 kByte NVRAM
Floating Point Unit	Yes
Max. Number of IMP participants	16
Response time per participant	4 µs
Rated voltage	24 V DC (18 ... 32 V)
Board supply $I_{MAX K24}$	1.0 A @ 24 V
Power consumption board supply	250 mA @ 24 V to K24
I/O supply $I_{MAX P24}$	10 A P24V (on-board fuse)
Power consumption I/O supply	5 mA @ 24 V to P24V
Operating temperature	0 ... +45 °C
Storage temperature	-20 ... 70 °C
Relative humidity	95%, no condensation
EMC	EN 61000-6-2 / EN 61000-6-4
Protection class	IP 20
Dimensions	114.5 × 99 × 45 mm (h × d × w)

Pin assignment



Indel-No.	Label & Option
612058400	IMP-MAS3 GIN