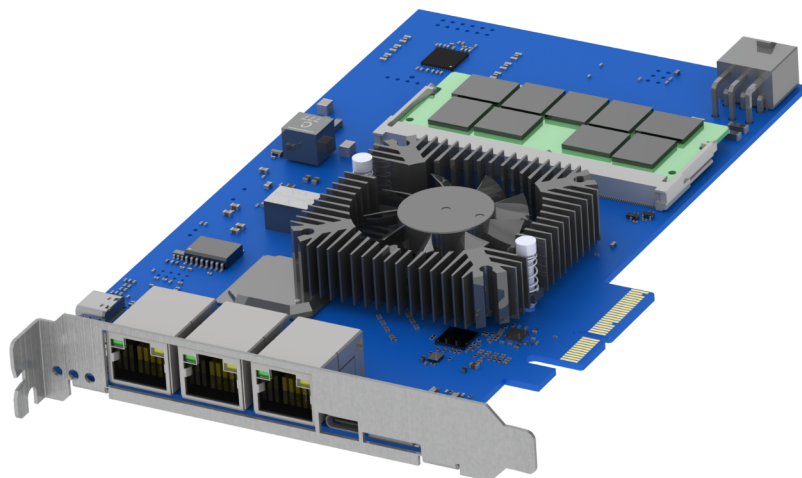


# GIN-PCIe5

## PCI Express

## GinLink

## Master



- ➔ GinLink Master
- ➔ 1.8 GHz Quad-Core CPU
- ➔ PCIe Gen 3.0

Facts	
PCIe standard	PCIe Gen 3.0 1 × Four lane < 25 watt power consumption
Interfaces	1 × GinLink 2 × Gigabit-Ethernet 1 × USB-C (console only) 1 × microSD
CPU	ARM Cortex-A72 1.8 GHz quad core
Memory	8 GByte SDRAM, DDR4-2133
MRAM	0.5 MByte
Flash	16 MByte
Operating system	Indel-Realtime-OS (INOS)
Motion control	Max. 256 axes Max. 32 kHz position loop
Length	180 mm

GIN-PCIe5 is a high-performance CPU board for use as a fieldbus master in the form of a PCIe plug-in card. Thanks to sophisticated CPU cooling, only a single PCIe 3.0 slot is required. The GinLink master is ideally suited for high-speed applications with high technical requirements.

As a fieldbus controller for the Indel Gigabit Ethernet fieldbus GinLink, GIN-PCIe5 can operate over 100 coordinated servo axes with a closed-loop bus frequency of up to 8 kHz thanks to its high transmission rate.

As with all other Indel CPU boards, OPC UA communication is implemented directly in the machine software. As OPC UA is flexible and completely platform-independent, it is considered the ideal communication protocol for implementing Industry 4.0.