

ARM Cortex-A9 IMP-Master



IMP-MAS3 is an IMP master with a powerful ARM CPU for applications of basic to medium complexity. As a fieldbus controller for INFO-Link, IMP-MAS3 controls and coordinates the entire periphery. In addition, up to 16 IMP participants can be directly addressed via the local IMP bus. The power supply for additional participants is already integrated into this module.

IMP-MAS3 INFO

**INFO-Link
IMP-Master
incl. 5V / 24V
Power Supply**

Technical data

Interfaces	1 × INFO-Link (Rx & Tx) 1 × Gigabit Ethernet 1 × SIO (RS232 / RS422) 1 × IMP-Bus
Real-time clock	Ja
CPU	ARM Cortex-A9 dual-core, 800 MHz
Memory	256 MByte DDR-RAM 8 MByte NOR-Flash 512 kByte NVRAM
Floating Point Unit	Yes
Max. Number of IMP participants	16
Response time per participant	4 µs
Rated voltage	24 V DC (18 ... 32 V)
Board supply I _{MAX} K24	1.0 A @ 24 V
Power consumption board supply	250 mA @ 24 V to K24
I/O supply I _{MAX} P24	10 A P24V (on-board fuse)
Power consumption I/O supply	5 mA @ 24 V to P24V
Operating temperature	0 ... +45 °C
Storage temperature	-20 ... 70 °C
Relative humidity	95%, no condensation
EMC	EN 61000-6-2 / EN 61000-6-4
Protection class	IP 20
Dimensions	114.5 × 99 × 45 mm (h × d × w)

Pin assignment

	X1 4	X1 3	X1 2	X1 1	X1
	RJ12				X2
SIO1	K0V	K24	P0V	P24V	
	SIO				
	INDEL				
	K24V		P24V		
	+5V		Fuse		
	IMP-MAS3				
	Ok		Err		
	Rec		+5V		
INFO-Link Master/Slave					
Receive					
Transmit					
	Ethernet				
X7	TORx 194				
X8	TORx 195				
	RJ45				X4

Indel-No.	Label & Option
612058425	IMP-MAS3 INFO